

# SAGA registers



## Overview

The SAGA Core has dedicated registers in the following ranges:

- DE000x
- DFF1Ex
- DFF1Fx
- DFF3xx
- DFF4xx to DFF7xx

These registers are mainly used by the SAGA Drivers (RTG and SD drivers) and some other tools.

## SAGA-specific registers

Below is the complete SAGA registers list:

Register	Name	Access	Size	Description
DE0000	<a href="#">saga_sdcard_data</a>	RW	UBYTE	Read and Write Data
DE0004	<a href="#">saga_sdcard_ctl</a>	W	UWORD	Write Control
DE0006	<a href="#">saga_sdcard_stat</a>	R	UWORD	Read Status
DE000C	<a href="#">saga_sdcard_clk</a>	W	UBYTE	Clock Speed
DE0008	<a href="#">saga_clock_counter</a>	R	ULONG	Read the number of cycles since last call
DFF1EC	<a href="#">saga_video_planepr</a>	W	ULONG	Set the SAGA Video FrameBuffer Address
DFF1F4	<a href="#">saga_video_mode</a>	W	UWORD	Set the SAGA Video Mode
DFF1F8	<a href="#">saga_video_pll</a>	W	UWORD	Set the SAGA ModeLine PIXELCLOCK
DFF1FA	<a href="#">saga_video_pll</a>	R	UWORD	Set the SAGA ModeLine PIXELCLOCK
DFF300	<a href="#">saga_video_hpixel</a>	W	UWORD	Set the SAGA ModeLine HPIXEL
DFF302	<a href="#">saga_video_hsstrt</a>	W	UWORD	Set the SAGA ModeLine HSSTRT
DFF304	<a href="#">saga_video_hsstop</a>	W	UWORD	Set the SAGA ModeLine HSSTOP
DFF306	<a href="#">saga_video_htotal</a>	W	UWORD	Set the SAGA ModeLine HTOTAL
DFF308	<a href="#">saga_video_vpixel</a>	W	UWORD	Set the SAGA ModeLine VPIXEL
DFF30A	<a href="#">saga_video_vsstrt</a>	W	UWORD	Set the SAGA ModeLine VSSTRT
DFF30C	<a href="#">saga_video_vsstop</a>	W	UWORD	Set the SAGA ModeLine VSSTOP
DFF30E	<a href="#">saga_video_vtotal</a>	W	UWORD	Set the SAGA ModeLine VTOTAL
DFF310	<a href="#">saga_video_hvsync</a>	W	UWORD	Set the SAGA ModeLine HVSYNC
DFF400	<a href="#">saga_video_clut [0]</a>	W	ULONG	Set the SAGA CLUT8 Color #001

Register	Name	Access	Size	Description
DFF7FC	<a href="#">saga_video_clut</a> [255]	W	ULONG	Set the SAGA CLUT8 Color #256
DFF1F8	<a href="#">saga_flash_write</a>	W	UWORD	SPI Flash Write data
DFF1FA	<a href="#">saga_flash_read</a>	R	UWORD	SPI Flash Read data

## Remarks

**Video PLL** and **SPI Flash** are mapped to the same registers.

This Bus is shared between several users. Selection is done by sending a **Magic Word**.

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## OCS/ECS/AGA registers

For more information about the original Amiga chipset's registers:

- [Amiga Hardware Reference](#)
- [Amiga Hardware Manual Guide](#)

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You are here: [start](#) » [saga](#) » [registers](#)

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